

DIC 5551 – a Golden Key To Open the Door of the CompactPCI Serial World

The CompactPCI Serial standard is a rather attractive solution for many equipment manufacturers striving to comply with the latest technical requirements. However, usually there are at least two obstacles to deal with: necessity to implement the "inherited" interfaces and to develop proprietary modules that perform unique functions. Previously, manufacturers had to develop CompactPCI Serial modules right "from the scratch", what was a challenge for numerous manufacturers. Now, there is Fastwel DIC551.

The module was created with mezzanine technology using <u>DIC551</u> carrier-boards and a set of MIC100x mezzanines. Functional capabilities of the module are determined by the installed mezzanines.

<u>DIC551</u> mezzanine carrier-board is implemented in the CompactPCI Serial 3U standard. The board is based on the FPGA with integrated PCIe x1 transceiver.

PCIe x1 is a system interface for connection with other modules of <u>CompactPCI Serial</u> system for DIC551. The carrier board enables having two mezzanine boards installed on it and ensures availability of rear and front input of signals from mezzanine.

The mezzanine is a complete board in terms of its functionality, ensuring analog and digital input-output and its implementation doesn't depend on the PC system bus used. This module communicates with the carrier-board using "simple" serial and parallel communication channels (SPI, SPORT, UART etc.). Interconnection of the "simple" communication channels and integration into the CompactPCI Serial standard system is carried out by the carrier-board.

System developer can either select standard Fastwel mezzanines or develop its own, which could be done relatively easy and fast. In this case, Fastwel experts undertake to manufacture DIC551 carrier-board and to develop drivers of the carrier for a set of operating systems.

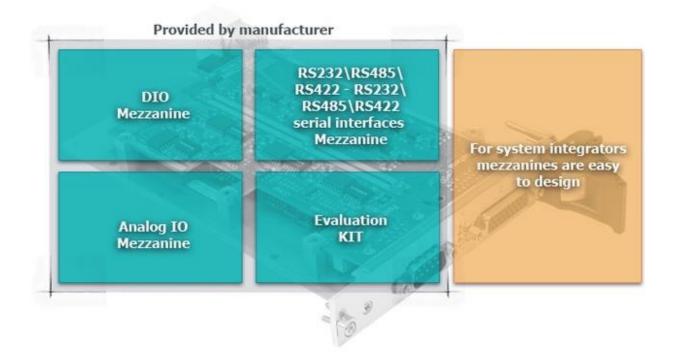
Fastwel supplies the following mezzanines:

MIC1002 – Mezzanine of RS232\RS485\RS422 interfaces is designed for carrying out of I/O operations via 4x individually galvanically isolated serial port of RS232\RS485\RS422 type and represents a board for converting signal levels from the carrier-board located UART, into the signal levels of the relevant standards.

MIC1003 – Digital I/O mezzanine, designed for input and output of 48 signals with CMOS logical levels. It is equipped with ESD protection. The mezzanine can be used for signal frequency measuring, receipt and transfer of serial code, conversion of codes, control of alphanumeric indicators, generation of time control charts.

MIC1004 – Mezzanine of current loop interfaces is designed for carrying out I/O operations via 4x individually galvanically isolated serial ports of current loop type and represents a board for converting signal levels from the carrier-board located UART, into the levels of signals that meet the current loop standard.

DIC551 carrier-board and MIC100x mezzanines can operate in the extended temperature range: from - 40° C to +85° C. They can be implemented in Eurocard format or other formats with conduction cooling (PICMG CPCI-S.0 R1.0 section 3)



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